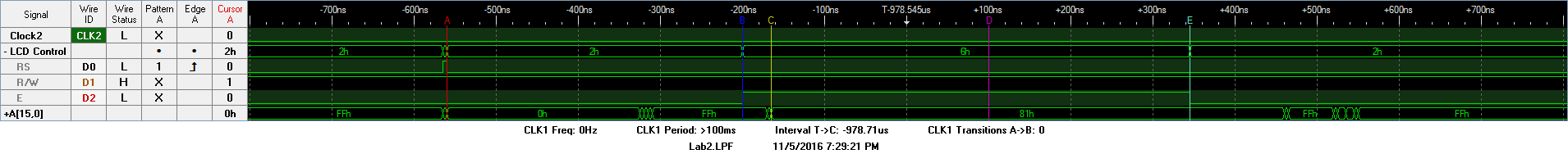
Lab 4 Writeup

Timing Analysis of LCD Control signals



Marker A indicates the last change of RS or R/W. Marker B indicates where the execute signal goes high. This is 380ns later, proving that it satisfies the 40ns Tas time for the LCD. Marker C indicates where the LCD provides valid data on this read. This is ~40ns which is less than the specified max of 160 ns. Marker E indicates where E goes low, because RD from the processor has gone back high, leaving enough time for the processor to latch the read data from the bus.